```
L00001    ent     sp    L00002
          ent     ep    L00003
          lda     0     5
          ldc     i     39
          sti     i
          rtn     p
#define   L00002  5
#define   L00003  2
          mst     0
          cup     0     L00001
          stp
```

Label Table

label	address	resolved	reference		
L00001	0	yes			
L00002	5	~~no~~ yes	0		
L00003	2	~~no~~ yes	1		

Integer Constants

Index	Value
0	39

Instruction Array

Address	op	r1	r2
0	ent(02)	sp(00)	5
1	ent(02)	ep(01)	2
2	lda(31)	00	0005
3	ldc(32)	i(03)	0
4	sti(3A)	i(03)	
5	rtn(04)	p(07)	
6	mst(03)		0
7	cup(00)	00	0
8	stp(30)		

Instruction Array

Address	op	r1	r2
0	02	00	0005
1	02	01	0002
2	31	00	0005
3	32	03	0000
4	3A	03	0000
5	04	07	0000
6	03	00	0000
7	00	00	0000
8	30	00	0000

Directory

Offset	Field	Value		
0	index of first instruction	6		
4	stack size	1024		
8	p-code instruction array offset	52		
12	p-code instruction array length	36		
16	integer constants offset	48		
20	integer constants length	4		
24	real constants offset	52		
28	real constants length	0		
32	set constants offset	52		
36	set constants length	0		
40	string constants offset	52		
44	string constants length	0		
48	Integer Constants	39		
52	p-code instruction array	02	00	0005
		02	01	0002
		31	00	0005
		32	03	0000
		3A	03	0000
		04	07	0000
		03	00	0000
		00	00	0000
		30	00	0000